

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3721	carrier\$2 and latch\$2 and (elastic\$4 sprin\$2)and (test\$4)and (via\$1 hole\$2 through window\$2)	US-PGPUB; USPAT	OR	ON	2006/02/06 04:38
L2	4415	((pass)adj2 (via\$2 hole\$2 window\$2 opening\$2))near11(communitc\$4 contact\$4 coupl\$4)	US-PGPUB; USPAT	OR	ON	2006/02/06 04:38
L3	5	(socket\$4 near2 test\$4)and 1 and 2	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:47
L4	1608	257/712 257/732 257/048.ccls.	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:41
L5	1	4 and 1 and 2	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:46
L6	5	4 and 1	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:41
L7	112724	(carrier adj module\$2 circuit adj board\$2 multichip\$2 chip\$2 "ic" semiconductor adj device\$2 integrated adj circuit die) with(hous\$4 cover\$4)	US-PGPUB; USPAT	OR	ON	2006/02/06 04:49
L8	6	4 and 7 and 2	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:47
L9	608	4 and 7	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:47
L10	2	(socket\$4 near2 test\$4)and 9	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:47
L11	0	latch\$2 and (elastic\$4 sprin\$2)and (test\$4).ti. and 9	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:48
L12	1	latch\$2 and (elastic\$4 sprin\$2)and (test\$4) and 9	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:48
L13	4	latch\$2 and (elastic\$4 sprin\$2) and 9	US-PGPUB; USPAT	OR	OFF	2006/02/06 04:50
L14	147313	(carrier adj module\$2 circuit adj board\$2 multichip\$2 chip\$2 "ic" semiconductor adj device\$2 integrated adj circuit die) and(hous\$4 cover\$4)	EPO; JPO; DERWENT	OR	ON	2006/02/06 04:49
L15	287	latch\$2 and (elastic\$4 sprin\$2) and 14	EPO; JPO; DERWENT	OR	OFF	2006/02/06 04:50
L16	402	(dissipat\$4 spread\$4 sink)and (elastic\$4 spring)and (lock\$4 latch\$2) and ((ring) (hous\$4 encapsul\$5))	EPO; JPO; DERWENT	OR	ON	2006/02/06 04:51
L17	7	15 and 16	EPO; JPO; DERWENT	OR	ON	2006/02/06 04:51